IXYS
INTEGRATED CIRCUITS DIVISION

CPC1217

Voltage-Controlled, Single-Pole, Normally Open, 4-Pin SIP OptoMOS® Relay

Parameter	Rating	Units
Blocking Voltage	60	V_P
Load Current	200	mA_{rms} / mA_{DC}
On-Resistance (max)	16	Ω
Input Voltage to operate	5-12	V

Features

- Designed for use in Security Systems Complying with EN50130-4
- Voltage-Controlled Operation
- 2500V_{rms} Input/Output Isolation
- 100% Solid State
- Matches Popular Reed Relay Pin-Out
- Arc-Free With No Snubbing Circuits
- No EMI/RFI Generation
- Immune to Radiated EM Fields
- Small 4-Pin SIP Package
- Auto Pick & Place, Wave Solderable

Applications

- Security
  - Passive Infrared Detectors (PIR)
  - Data Signalling
  - Sensor Circuitry
- Instrumentation
  - Multiplexers
  - Data Acquisition
  - Electronic Switching
  - I/O Subsystems
- Energy Meters
- Medical Equipment—Patient/Equipment Isolation
- Aerospace
- Industrial Controls

Description

The CPC1217 is a voltage-controlled, single-pole, normally open (1-Form-A), optically coupled solid state relay configuration in a 4-pin Single In-line Package (SIP). IXYS Integrated Circuits Division's patented OptoMOS architecture makes available the optically coupled technology necessary to activate the output's efficient MOSFET switches while providing a 2500V_{rms} input-to-output isolation barrier. Control of the isolated output is accomplished by means of a highly effective GaAlAs infrared LED at the input while the internal resistor in series with the LED enables the input's voltage-controlled operation.

Because the input is solid state there is no need for snubbers or "catch" diodes to suppress the inductive flyback transient voltage normally associated with EMR coils.

Approvals

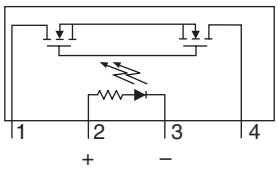
- UL 508 Approved Component: File E69938
- CSA Certified Component: Certificate 1172007

Ordering Information

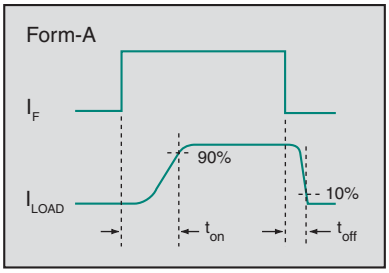
Part #	Description
CPC1217Y	4-Pin SIP (8-Pin Body) (25/tube)

Pin Configuration

CPC1217 Pinout



Switching Characteristics of Normally Open Devices



Absolute Maximum Ratings @ 25ºC

Parameter	Ratings	Units
Blocking Voltage	60	V_P
Reverse Input Voltage	5	V
Input Control Voltage	15	V
Input Power Dissipation	225	mW
Total Power Dissipation [1]	800	mW
Isolation Voltage, Input to Output	2500	V_{rms}
Operational Temperature	-40 to +85	°C
Storage Temperature	-40 to +125	°C

[1] Derate linearly 6.67 mW / ºC

Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

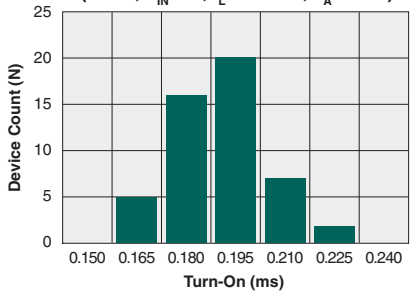
Electrical Characteristics @ 25ºC

Parameter	Conditions	Symbol	Min	Typ	Max	Units
Output Characteristics						
Load Current						
Continuous	V_{IN}=5V	I_L	-	-	200	mA_{rms} / mA_P
Peak	t≤10ms	I_{LPK}	-	-	±400	mA_P
On-Resistance [1]	I_L=200mA	R_{ON}	-	-	16	Ω
Off-State Leakage Current	V_L=60V_P	I_{LEAK}	-	-	1	µA
Switching Speeds						
Turn-On (Output Closed)	V_{IN}=5V, V_L=10V	t_{on}	-	-	5	ms
Turn-Off (Output Open)		t_{off}	-	-	5	
Output Capacitance	V_{IN}=0V, V_L=50V, f=1MHz	C_{OUT}	-	25	-	pF
Input Characteristics						
Input Control Voltage						
Output Closed	I_L=200mA	V_{IN}	-	-	3.75	V
Recommended Operating Range			5	-	12	
Output Open			1	-	-	
Reverse Input Current	V_{IN}=-5V	I_R	-	-	10	µA
Input Resistor	-	-	900	1000	1100	Ω
Common Characteristics						
Capacitance, Input to Output	-	-	-	1	-	pF

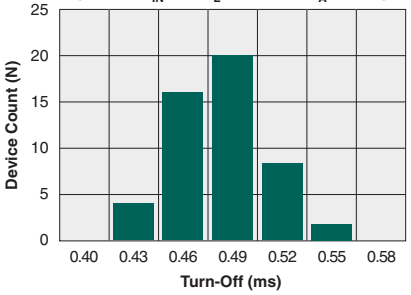
[1] Measurement taken within 1 second of on-time.

PERFORMANCE DATA*

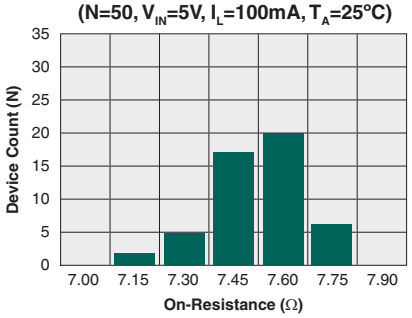
Typical Turn-On Time
(N=50, V_{IN}=5V, I_L=100mA, T_A=25°C)



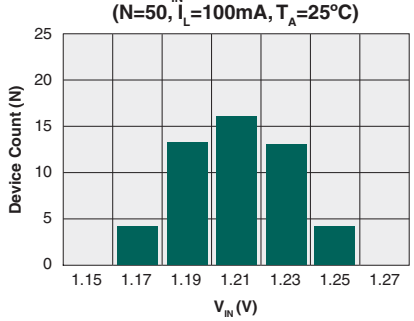
Typical Turn-Off Time
(N=50, V_{IN}=5V, I_L=100mA, T_A=25°C)



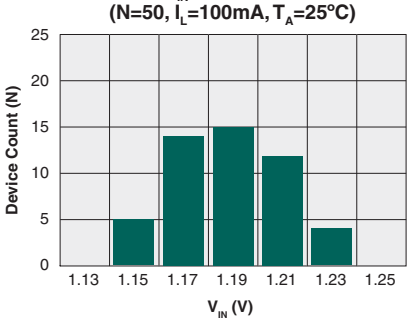
Typical On-Resistance Distribution
(N=50, V_{IN}=5V, I_L=100mA, T_A=25°C)



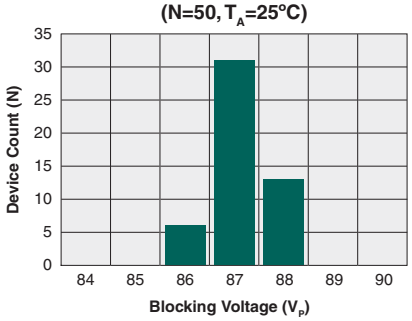
Typical V_{IN} for Switch Operation
(N=50, I_L=100mA, T_A=25°C)



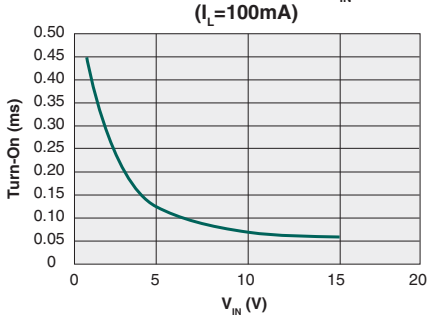
Typical V_{IN} for Switch Dropout
(N=50, I_L=100mA, T_A=25°C)



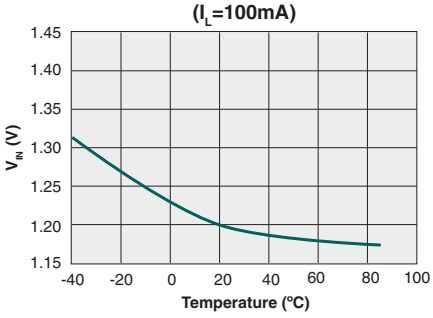
Typical Blocking Voltage Distribution
(N=50, T_A=25°C)



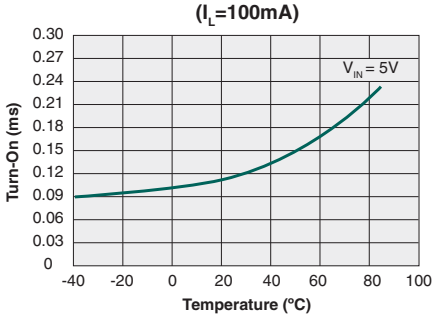
Typical Turn-On vs. V_{IN}
(I_L=100mA)



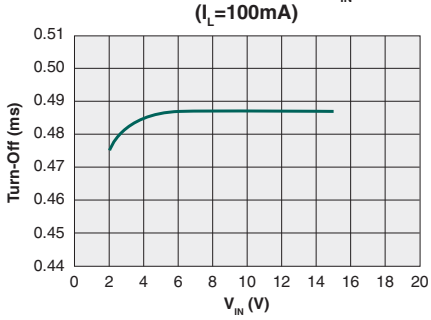
Typical V_{IN} for Switch Operation
vs. Temperature
(I_L=100mA)



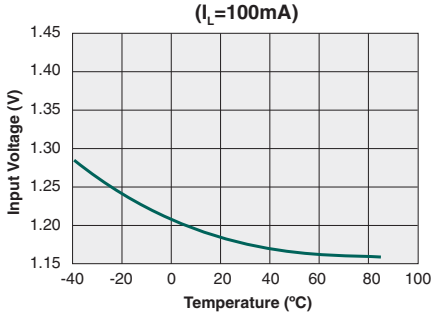
Typical Turn-On vs. Temperature
(I_L=100mA)



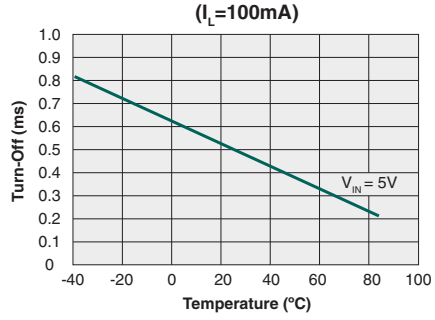
Typical Turn-Off vs. V_{IN}
(I_L=100mA)



Typical V_{IN} for Switch Dropout
vs. Temperature
(I_L=100mA)



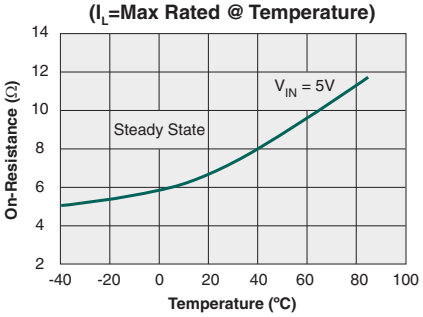
Typical Turn-Off vs. Temperature
(I_L=100mA)



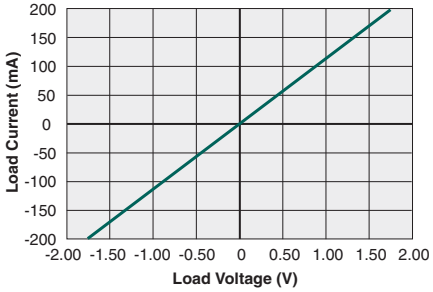
*The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

PERFORMANCE DATA*

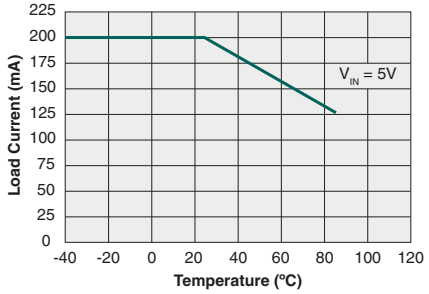
Typical On-Resistance vs. Temperature
(I_L=Max Rated @ Temperature)



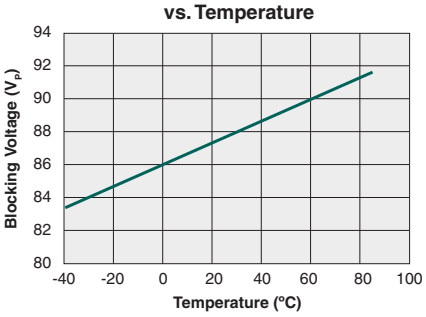
Typical Load Current vs. Load Voltage
(V_{IN}=5V, T_A=25°C)



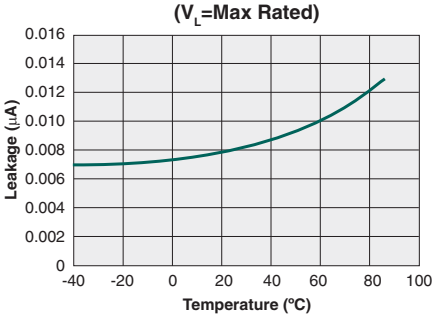
Typical Maximum Load Current vs. Temperature



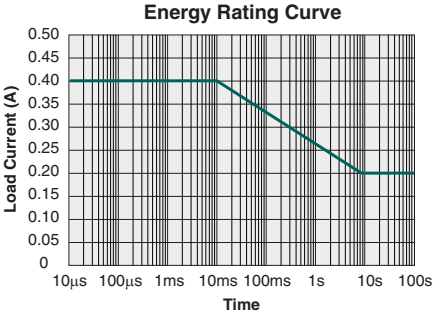
Typical Blocking Voltage vs. Temperature



Typical Leakage vs. Temperature
Measured Across Pins 1&4)
(V_L=Max Rated)



Energy Rating Curve



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Manufacturing Information

Moisture Sensitivity

All plastic encapsulated semiconductor packages are susceptible to moisture ingression. IXYS Integrated Circuits Division classified all of its plastic encapsulated devices for moisture sensitivity according to the latest version of the joint industry standard, **IPC/JEDEC J-STD-020**, in force at the time of product evaluation. We test all of our products to the maximum conditions set forth in the standard, and guarantee proper operation of our devices when handled according to the limitations and information in that standard as well as to any limitations set forth in the information or standards referenced below.

Failure to adhere to the warnings or limitations as established by the listed specifications could result in reduced product performance, reduction of operable life, and/or reduction of overall reliability.

This product carries a **Moisture Sensitivity Level (MSL) rating** as shown below, and should be handled according to the requirements of the latest version of the joint industry standard **IPC/JEDEC J-STD-033**.

Device	Moisture Sensitivity Level (MSL) Rating
CPC1217Y	MSL 1

ESD Sensitivity

This product is **ESD Sensitive**, and should be handled according to the industry standard **JESD-625**.

Reflow Profile

This product has a maximum body temperature and time rating as shown below. All other guidelines of **J-STD-020** must be observed.

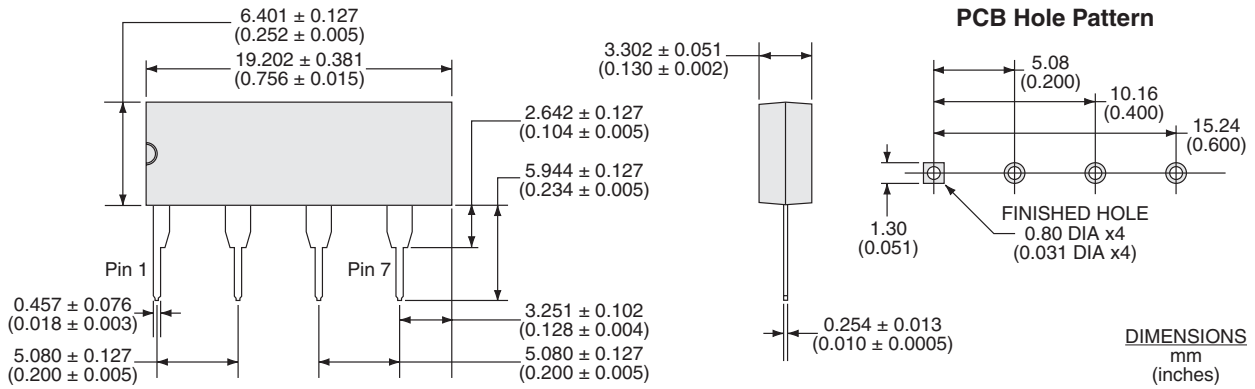
Device	Maximum Temperature x Time
CPC1217Y	245°C for 30 seconds

Board Wash

IXYS Integrated Circuits Division recommends the use of no-clean flux formulations. However, board washing to remove flux residue is acceptable. Since IXYS Integrated Circuits Division employs the use of silicone coating as an optical waveguide in many of its optically isolated products, the use of a short drying bake could be necessary if a wash is used after solder reflow processes. Chlorine- or Fluorine-based solvents or fluxes should not be used. Cleaning methods that employ ultrasonic energy should not be used.

MECHANICAL DIMENSIONS

CPC1217Y



For additional information please visit our website at: www.ixysic.com

Specification: DS-CPC1217-R05

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