



PIC12F529T48A

PIC12F529T48A Family Silicon Errata and Data Sheet Clarification

The PIC12F529T48A family devices that you have received conform functionally to the current Device Data Sheet (DS41634A), except for the anomalies described in this document.

The silicon issues discussed in the following pages are for silicon revisions with the Device and Revision IDs listed in [Table 1](#). The silicon issues are summarized in [Table 2](#).


The errata described in this document will be addressed in future revisions of the PIC12F529T48A silicon.

Note: This document summarizes all silicon errata issues from all revisions of silicon, previous as well as current. Only the issues indicated in the last column of [Table 2](#) apply to the current silicon revision (N/A).

Data Sheet clarifications and corrections start on page 2, following the discussion of silicon issues.

The silicon revision level can be identified using the current version of MPLAB® IDE and Microchip's programmers, debuggers, and emulation tools, which are available at the Microchip corporate web site (www.microchip.com).

For example, to identify the silicon revision level using MPLAB IDE in conjunction with a hardware debugger:

1. Using the appropriate interface, connect the device to the hardware debugger.
2. Open an MPLAB IDE project.
3. Configure the MPLAB IDE project for the appropriate device and hardware debugger.
4. Based on the version of MPLAB IDE you are using, do one of the following:
 - a) For MPLAB IDE 8, select *Programmer > Reconnect*.
 - b) For MPLAB X IDE, select *Window > Dashboard* and click the **Refresh Debug Tool Status** icon ().
5. Depending on the development tool used, the part number *and* Device Revision ID value appear in the **Output** window.

Note: If you are unable to extract the silicon revision level, please contact your local Microchip sales office for assistance.

The DEVREV values for the various PIC12F529T48A silicon revisions are shown in [Table 1](#).

TABLE 1: SILICON DEVREV VALUES

Part Number	Device ID	Revision ID for Silicon Revision
PIC12F529T48A	N/A	N/A

PIC12F529T48A

TABLE 2: SILICON ISSUE SUMMARY

Module	Feature	Item Number	Issue Summary	Affected Revisions
None	N/A	N/A	No known issues.	N/A

Silicon Errata Issues

Note: This document summarizes all silicon errata issues from all revisions of silicon, previous as well as current. Only the issues indicated by the shaded column in the following tables apply to the current silicon revision (**N/A**).

None.

Data Sheet Clarifications

The following typographic corrections and clarifications are to be noted for the latest version of the device data sheet (DS41634A):

Note: Corrections are shown in **bold**. Where possible, the original bold text formatting has been removed for clarity.

1. Module: RF Transmitter

The following text has been added to **Section 9.1.2 “Advanced Mode”**:

“It is not possible to reliably reset the transmitter configuration state machine from an unknown state without power-cycling the device. Ensure that transient signals are not emitted on the two-wire communications interface (TWI) during device power-up or operation.”

APPENDIX A: DOCUMENT REVISION HISTORY

Rev A Document (06/2014)

Initial release of this document.

Note the following details of the code protection feature on Microchip devices:

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